



Patent
246/214
19535-7006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Alexander D. Schapira et al.

Serial No.: 09/648,540

Filed: August 28, 2000

For: METHOD AND SYSTEM FOR
SIMULATION OF DIGITAL/ANALOG
INTERFACES WITH ANALOG TRI-STATE
IOPUTS

Group Art Unit: 2768

POWER OF ATTORNEY

AND REVOCATION OF PREVIOUS POWERS

Commissioner for Patents
Washington, D.C. 20231

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Cadence Design Systems, Inc., a Delaware corporation, having a place of business at 2655 Seely Avenue, San Jose, California 95134, certifies that to the best of its knowledge and belief it is the assignee or is entitled to ownership of the entire right, title, and interest in and to the above-referenced patent application by virtue of an Assignment recorded on February 26, 2001 at Reel 011553, Frame 0936, and represents that the undersigned is a representative authorized and empowered to sign on behalf of Cadence Design Systems, Inc., which hereby revokes all powers of attorney previously given and appoints all the attorney(s) and/or agent(s) identified by:

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San Francisco, CA 94111-4067

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with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. Send all future correspondence to the attention of Peter C. Mei, Reg. No. 39,768, care of the above address and direct all telephone calls to the same at (650) 849-4870.

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Cadence Design Systems, Inc.

Dated: 1/15/03

By: 

Name: Peter Chen

Title: Vice President and

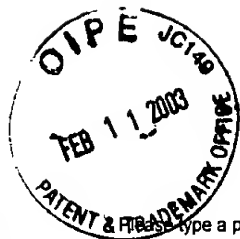
Associate General Counsel

Address of Assignee of Interest:

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Total Number Of Documents In This Submission

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Application Number

09/648,540

Filing Date

August 28, 2000

First Named Inventor

Alexander D. Schapira et al.

Group Art Unit

2768

Examiner Name

Not yet assigned

Attorney Docket No.

246/214; 19535-7006

ENCLOSURES (check all that apply)

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February 5, 2003

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